IN THE CLAIMS:

Please amend claim 12 as follows:

LISTING OF CURRENT CLAIMS

1-11. (Canceled)

12. (Currently Amended) A solid state disk module comprising:

a) an IDE interface for connecting the disk module to a main board of a computer;

b) a flash memory controller ~~for~~ electrically connected to the IDE interface, and controlling data access and specifying an address for data storage;

c) a flash memory array having a plurality of flash memories, the flash memory array being connected to the flash memory controller for saving data, the flash memory controller and the flash memory array are electrically connected to a circuit board, the flash memory controller and the flash memory array are enclosed by a casing; and

d) a power source having a power input terminal and a power output terminal and connected to the flash memory controller and the flash memory array to supply a working voltage.

13. (Previously Presented) The solid state disk module according to claim 12, wherein the power source is integrally formed with the IDE interface as a single connector.

14. (Previously Presented) The solid state disk module according to claim 12, wherein the flash memory controller is a single chip controller.

15. (Previously Presented) The solid state disk module according to claim 12, wherein the flash memory controller is an MX9691 controller.

16. (Previously Presented) The solid state disk module according to claim 12, wherein the plurality of flash memories comprises ten flash memories divided into five groups.

17. (Canceled)

18. (Canceled)

19. (Previously Presented) The solid state disk module according to claim 12, wherein the IDE interface is electrically connected to the circuit board.

20. (Previously Presented) The solid state disk module according to claim 14, wherein the IDE interface has an extending interface.